

**AMENDMENTS TO THE ABSTRACT**

Please change the abstract as follow:

The present invention is a data transfer circuit applicable to a liquid crystal display apparatus with a drive circuit formed integrally, for example, on an insulation substrate, and configured such that only an inverted output of a latch result of a first latch section (41) or only a non-inverted output thereof is data-transferred to a second latch section (42); and, that at least during a period of data transfer to the second latch section (42), a power supply voltage of the first latch section (41) is raised.